

RJ-11

SPECIFICATION Ver 0.01

CONFIDENTIAL

RJ Electronics

Document #: RJ11-S01

TABLE OF CONTENTS

CONTENTS

REVISION HISTORY	오류! 책갈피가 정의되어 있지 않습니다.
TABLE OF CONTENTS	2
FEATURES	3
APPLICATIONS	4
PACKAGE OUTLINES	6

CONFIDENTIAL

DESCRIPTION

RJ-11 is Image Signal Processor (ISP) which outputs CVBS after supporting YE, CY, MG and G single CCD color cameras. RJ-11, equipped with built-in MCU, has OSD menu which can be changeable by User.

RJ-11 is built-in Timing generator (TG), Pre-processor, Y-processor, C-processor, and NTSC/PAL Encoder, Optical parameter detector, MCU, peripheral interfaces and memory.

And RJ-11 has Digital-WDR, Real-WDR and 2DNR, 3DNR to reduce the noise and Sense-up function.

And then as further function there are Digital-Zoom, Freeze, Mirror (V, H), and etc.

This chip is high flexible for various surveillance cameras.

FEATURES

- **INPUT : NTSC/PAL 760H Mg, G, Cy, Ye Complementary Color CCD**
- **OUTPUT : NTSC/PAL CVBS and ITU-R.656 Digital Output**
- **3.3V HCCD, RG Pulse Driver**
- **H/V Aperture**
- **Mirror Function : Horizontal, Vertical**
- **Image Adjustment : Brightness, Hue, Gain and etc.)**
- **Lens Shading Correction.**
- **Adaptive Defect Correction : Auto, Manual Mode (points quantities depend by Memory space)**
- **False Color Suppression**
- **Digital Noise Reduction : 2DNR, 3DNR**
- **Supports High Resolution : 540TV lines**
- **Programmable Gamma**
- **Color Correction Logic**
- **OSD 128 Programmable Fonts**
- **Privacy Masking**
- **Motion Detection**
- **Secure Programming Memory**
- **Wide Dynamic Range : D-WDR (RJ11, RJ13), Real-WDR (RJ11, RJ13)**
- **Digital Zoom**
- **Sense-up**
- **8 bit 2 MHz Mixed 4 Input ADC**
- **Digital L/L**
- **SPI (Serial Peripheral Interface)**

APPLICATIONS

- Surveillance CCD Camera
- Industrial CCD Camera
- And etc.

RELATED CHIPS

- CCD : 760H Color-IT CCD

RJ-11		RJ-13
SONY CCD	SHARP CCD	SONY CCD
ICX638(639) Series	RJ2351(2361) Series	ICX212(213) Series

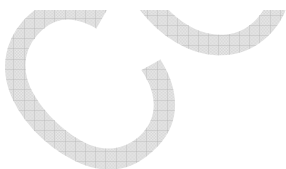
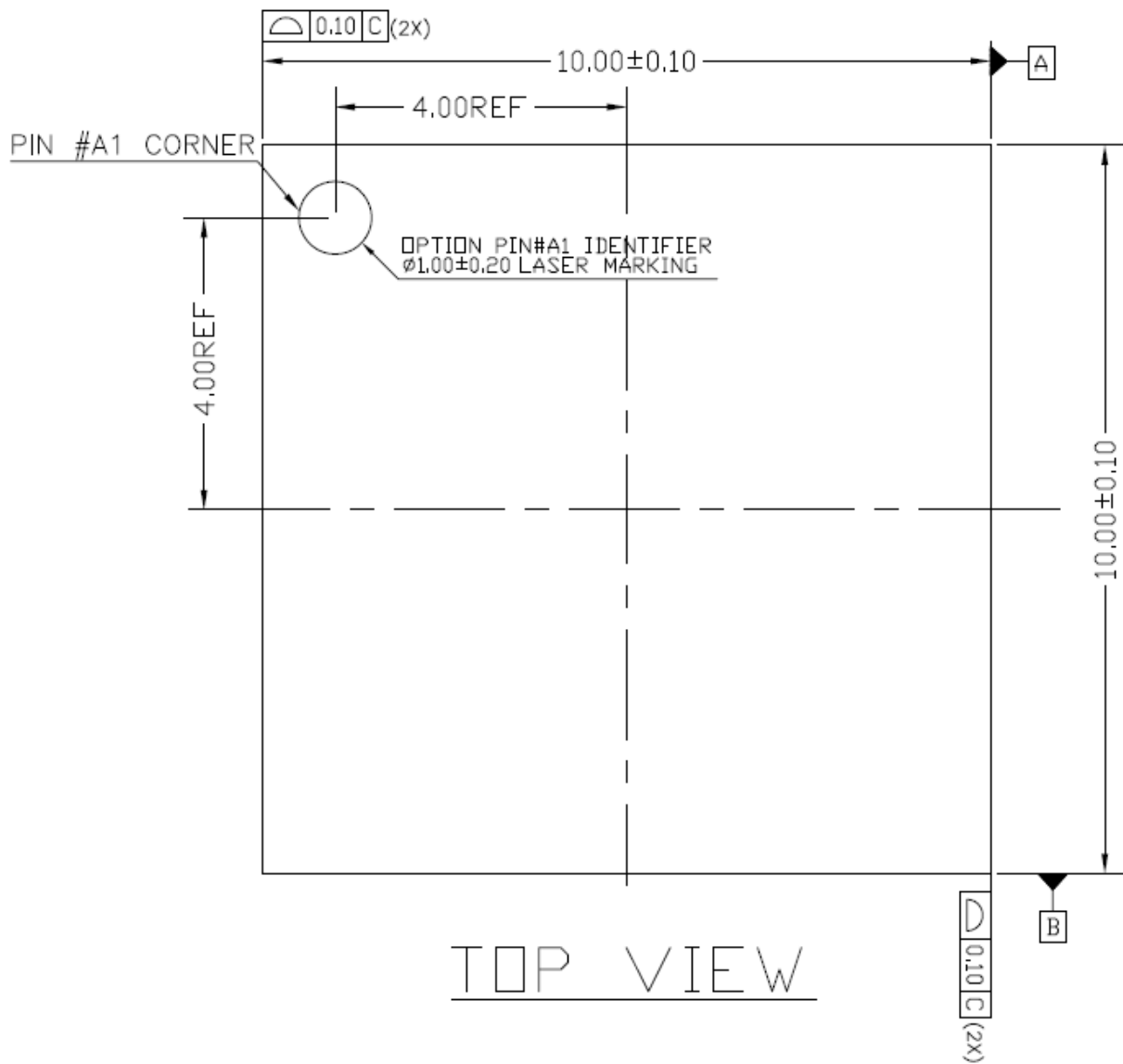
- AFE

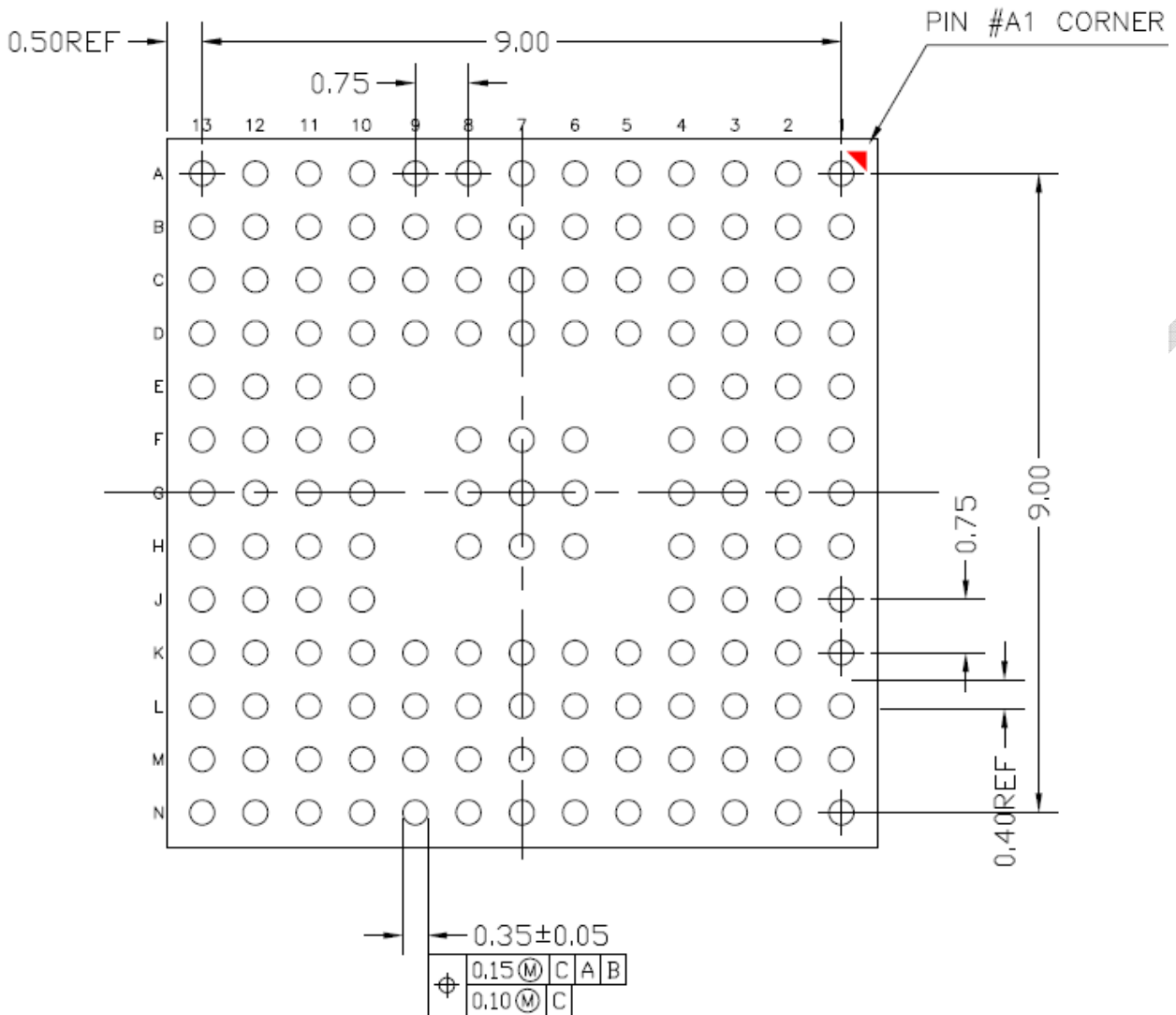
RJ-11	RJ-13
AD9943	AD9949

PIN CONFIGURATION

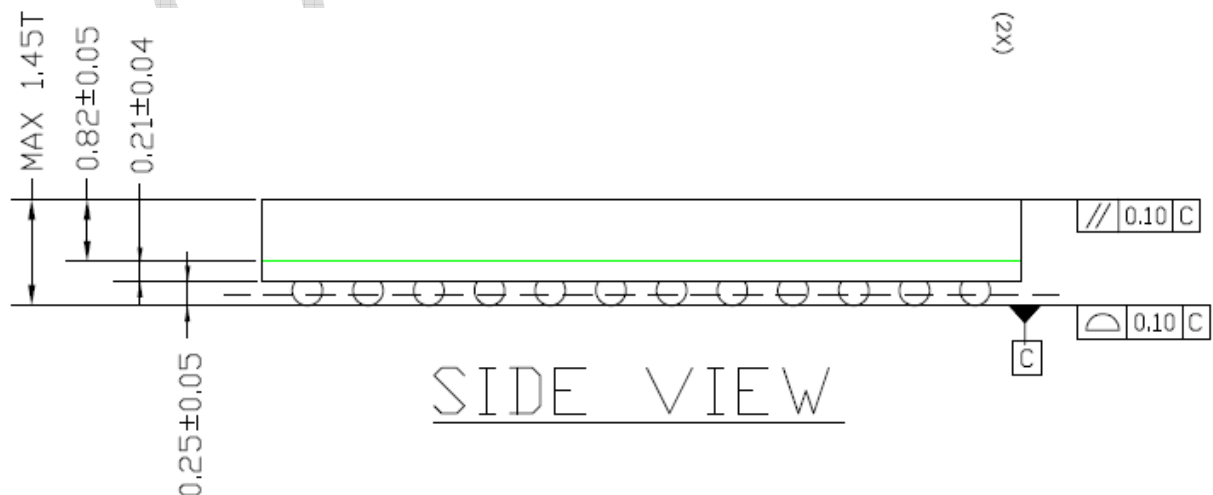
	1	2	3	4	5	6	7	8	9	10	11	12	13										
A	PC0	XSG1	XV1	VCCM2	VSS	PDN3	PDN5	VCCIO	VSS	VCCM2	VRI	PWM1	PC2	A									
B	PB7	PB6	XV2	RG	H1	PDN4	RES2	RESETEN	TXO	RXI	PWM2	PC3	PC1	B									
C	PDN2	XSG2	XV3	XSUB	H2	PDN6	PUP0	SCL	SDA	PWM3	VSS	VGX	IREFO	C									
D	PDN1	XV4	PB5	VCCINT	VSS	VCCM1	VCCIO	RES3	VCCINT	VCCM1	VSS	VREF	IREFI	D									
E	VSS	AVSS	MSCK	MSL	<table border="1" style="margin: auto;"> <tr> <td>VCCIO</td> <td>VCCINT</td> <td>VCCIO</td> </tr> <tr> <td>VSS</td> <td>REV0</td> <td>VSS</td> </tr> <tr> <td>VCCIO</td> <td>VSS</td> <td>VCCIO</td> </tr> </table>					VCCIO	VCCINT	VCCIO	VSS	REV0	VSS	VCCIO	VSS	VCCIO	DOUT0	AVDD3	AVSS	IOY	E
VCCIO	VCCINT	VCCIO																					
VSS	REV0	VSS																					
VCCIO	VSS	VCCIO																					
F	XCKI	XCKO	BG_CAP	BG_REF						DOUT2	AVDD3	AVSS	VGC	F									
G	AD0	AD1	CLPOB	MSDATA	PUP1	DOUT3	DOUT1	IOC	G														
H	SHP	SHD	AD3	RES1	DOUT4	DOUT6	DOUT7	DOUT5	H														
J	ADCLK	AD2	AD6	PBLK	VCCINT	PC5	PC4	DCLK	J														
K	REV5	AD4	AD9	VCCIO	VSS	VCCIO	VCCINT	KEYIN1	KEYIN0	VSS	AVSS	AVDD1	PWM0	K									
L	REV3	AD5	AD11	VCCINT	REV7	VSS	NC	VSS	VSS	VCCIO	AVDD1	ADCCAP	AVSS	L									
M	REV1	AD7	REV2	REV6	VCCM1	NC	NC	VCCM1	VSS	KEYIN3	ADCTR	ADC1	ADC0	M									
N	AD10	AD8	REV4	VCCM2	VSS	NC	NC	VSS	VCCM2	KEYIN4	KEYIN2	ADC2	ADC3	N									
	1	2	3	4	5	6	7	8	9	10	11	12	13										

PACKAGE OUTLINES





BOTTOM VIEW



SIDE VIEW